

Notice of Allowability

Application No.

10/674,190

Examiner

Stephen J. Cherry

Applicant(s)

KULIDJIAN ET AL.

Art Unit

2863

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 5-23-2007.
2. ☒ The allowed claim(s) is/are 1, 3-5, 7-20.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|--|---|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____ |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____ | 7. <input checked="" type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____ |

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr Reckamp on 7-27-2007.

The application has been amended as follows:

Please delete claim 1 and replace with:

1. A method for automated testing of display signals from video graphics circuitry comprising:

capturing at least one display signal from the video graphics circuitry;

converting the at least one display signal into at least one data acquisition signal having a pixel clock signal; and

providing the at least one data acquisition signal to a test system that is not part of said video graphics circuitry, wherein the test system tests the at least one display signal by taking time interval measurements of the at least one data acquisition signal, wherein at least one time interval measurement is based on the pixel clock signal.

Please delete claim 9 and replace with:

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9. A method for automated testing of display information for a display device

comprising:

providing a test command to a computer under test such that the computer under test generates display signals to be transmitted to the display device:

capturing at least one display signal from the computer under test;

converting the at least one display signal into at least one data acquisition signal having a pixel clock signal;

providing the at least one data acquisition signal from the computer under test to a test system that is not part of the computer under test; and

using the test system to take time interval measurements of the at least one data acquisition signal, wherein at least one time interval measurement is based on the pixel clock signal.

The following is an examiner's statement of reasons for allowance:

Claim 1 recites, "converting the at least one display signal into at least one data acquisition signal having a pixel clock signal; and providing the at least one data acquisition signal to a test system that is not part of said video graphics circuitry, wherein the test system tests the at least one display signal by taking time interval

measurements of the at least one data acquisition signal, wherein at least one time interval measurement is based on the pixel clock signal". This feature, in combination with remaining claimed structure, overcomes the prior art of record.

Claim 9 recites, "converting the at least one display signal into at least one data acquisition signal having a pixel clock signal; providing the at least one data acquisition signal from the computer under test to a test system that is not part of the computer under test; and using the test system to take time interval measurements of the at least one data acquisition signal, wherein at least one time interval measurement is based on the pixel clock signal". This feature, in combination with remaining claimed structure, overcomes the prior art of record.

Claim 14 recites, "a test computer configured to receive the data acquisition signal from the printed circuit board and to take time interval measurements of the data acquisition signal, wherein at least one time interval measurement is based on the pixel clock signal". This feature, in combination with remaining claimed structure, overcomes the prior art of record.

Claim 19 recites, "a test computer operably coupled to the printed circuit board, the test computer including a processor operably coupled to a memory storing executable instructions such that the processor, in response to the executable instructions, takes time interval measurements of the data acquisition signal, wherein at least one time interval measurement is based on the pixel clock signal". This feature, in combination with remaining claimed structure, overcomes the prior art of record.

The reason for allowance of claims 5, 17, and 18 is as indicated in the Office action dated 7-31-2006.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Stephen J. Cherry whose telephone number is (571) 272-2272. The examiner can normally be reached on M-F 8:00-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Barlow can be reached on (571) 272-2269. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

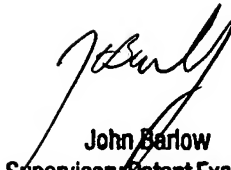
Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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SJC



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